

INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Not for submission under 37 CFR 1.99)	Application Number		10550323
	Filing Date		2005-09-23
	First Named Inventor	Roger D. Chamberlain	
	Art Unit	2134 2162	
	Examiner Name	M. T. Henning JBF	
Attorney Docket Number		53047-57370	

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	2	3611314	A	1971-10-05	Pritchard, Jr. et al.	
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9	4314356	A	1982-02-02	Scarbrough	
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	3	0887723	EP		1998-12-30	International Business Machines Corporation		<input type="checkbox"/>
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	5	0122425	WO	A	2001-03-29	Seagate Technology LLC		<input type="checkbox"/>
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	1	"Lucent Technologies Delivers "Payload Plus" Network Processors for Programmable, Multi-Protocol, OC-48c Processing", Lucent Technologies Press Release, downloaded from http://www.lucent.com/press/1000/0010320.meb.html on March 21, 2002. All pages.	<input type="checkbox"/>

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2	"Overview, Field Programmable Port Extender", January 2002 Gigabit Workshop Tutorial, Washington University, St. Louis, MO, January 3-4, 2002. All Pages.	<input type="checkbox"/>
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